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In re the Application of: HAEMATSU, Hitoshi

Group Art Unit: 2822

Serial No.: 10/659,337

Examiner: Pamela E. PERKINS

Filed: September 11, 2003

P.T.O. Confirmation No.: 7094

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

## AMENDMENT UNDER 37 CFR §1.116 - EXPEDITED RESPONSE GROUP ART UNIT 2822

## **MAIL STOP AF**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

November 18, 2005

Sir:

In response to the Final Office Action dated **September 6, 2005**, please amend the above-identified application as follows: